

FIG. 1 is a block diagram of a system 10 for secure communication between a remote device 12 and a device 14. The system 10 includes an interface 20 for the remote device 12, which is connected to a controller 16. The controller 16 includes a random number generator (HRNG) 18. The interface 20 is also connected to a security module 22. The device 14 is connected to the interface 22. The system 10 is connected to a manufacturer/third party 26 via the Internet 28. The system 10 is also connected to a biometric ID 24 via the Internet 28.

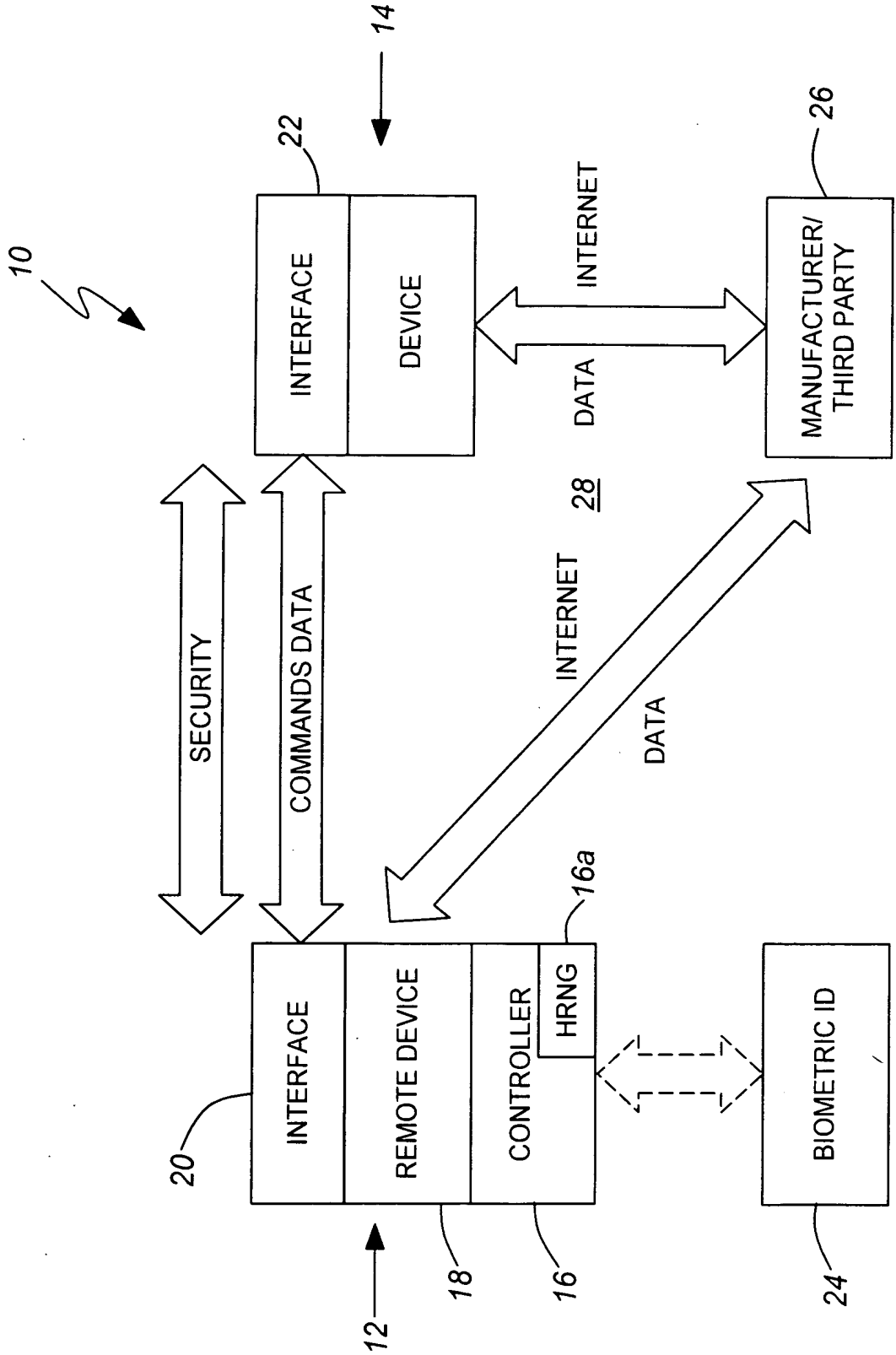


FIG. 1

FIG. 2 is a block diagram of a system 10. The system 10 includes a central processing unit 12, a memory unit 14, a random number generator 16a, a pass-thru parallel printer port 18, a pass-thru RS232C port 20, and a hardware random number generator 16a. The central processing unit 12 is connected to the memory unit 14, the random number generator 16a, the pass-thru parallel printer port 18, and the pass-thru RS232C port 20. The memory unit 14 is connected to the random number generator 16a. The random number generator 16a is connected to the pass-thru parallel printer port 18. The pass-thru parallel printer port 18 is connected to the pass-thru RS232C port 20. The pass-thru RS232C port 20 is connected to a host serial port and a target serial port.

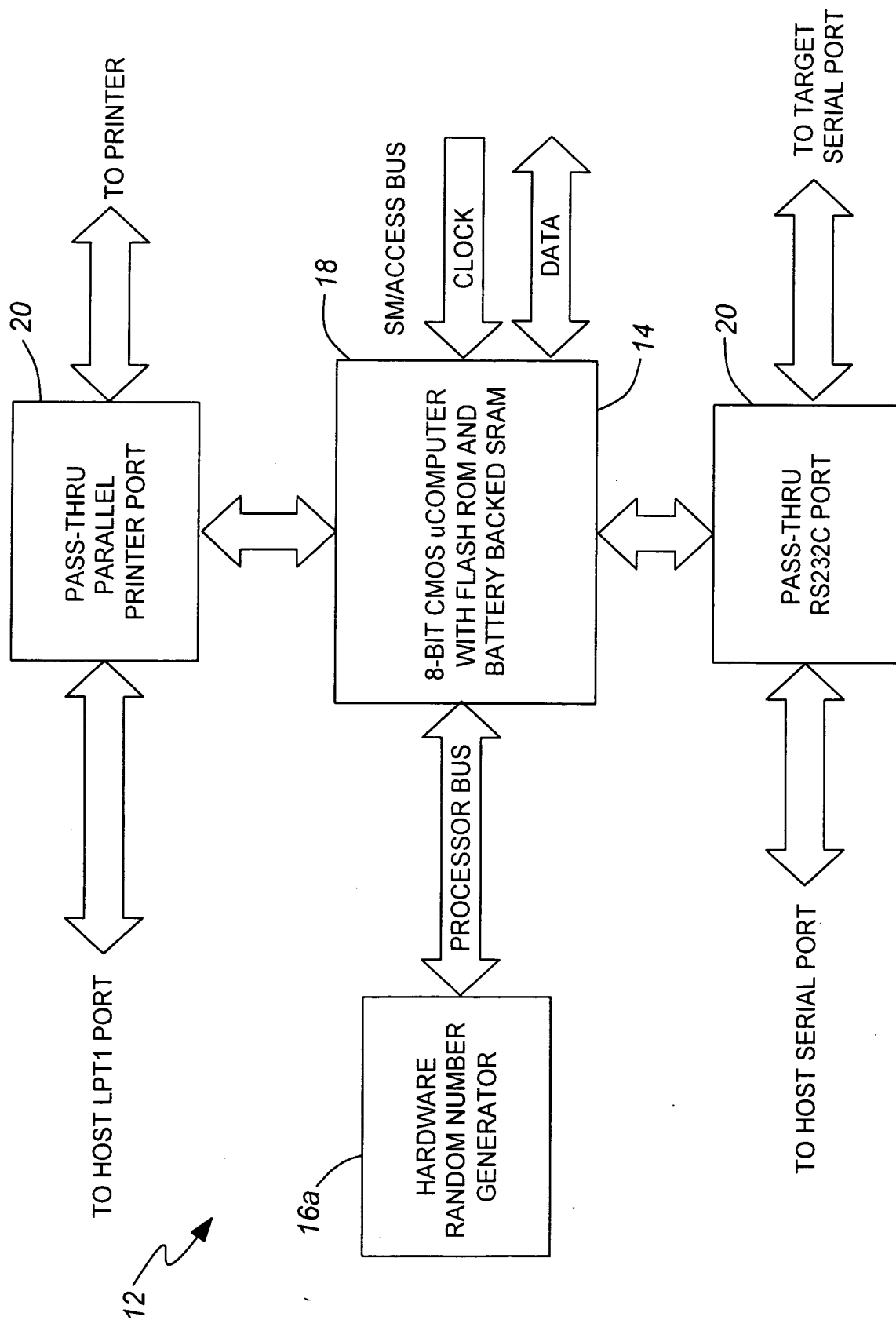
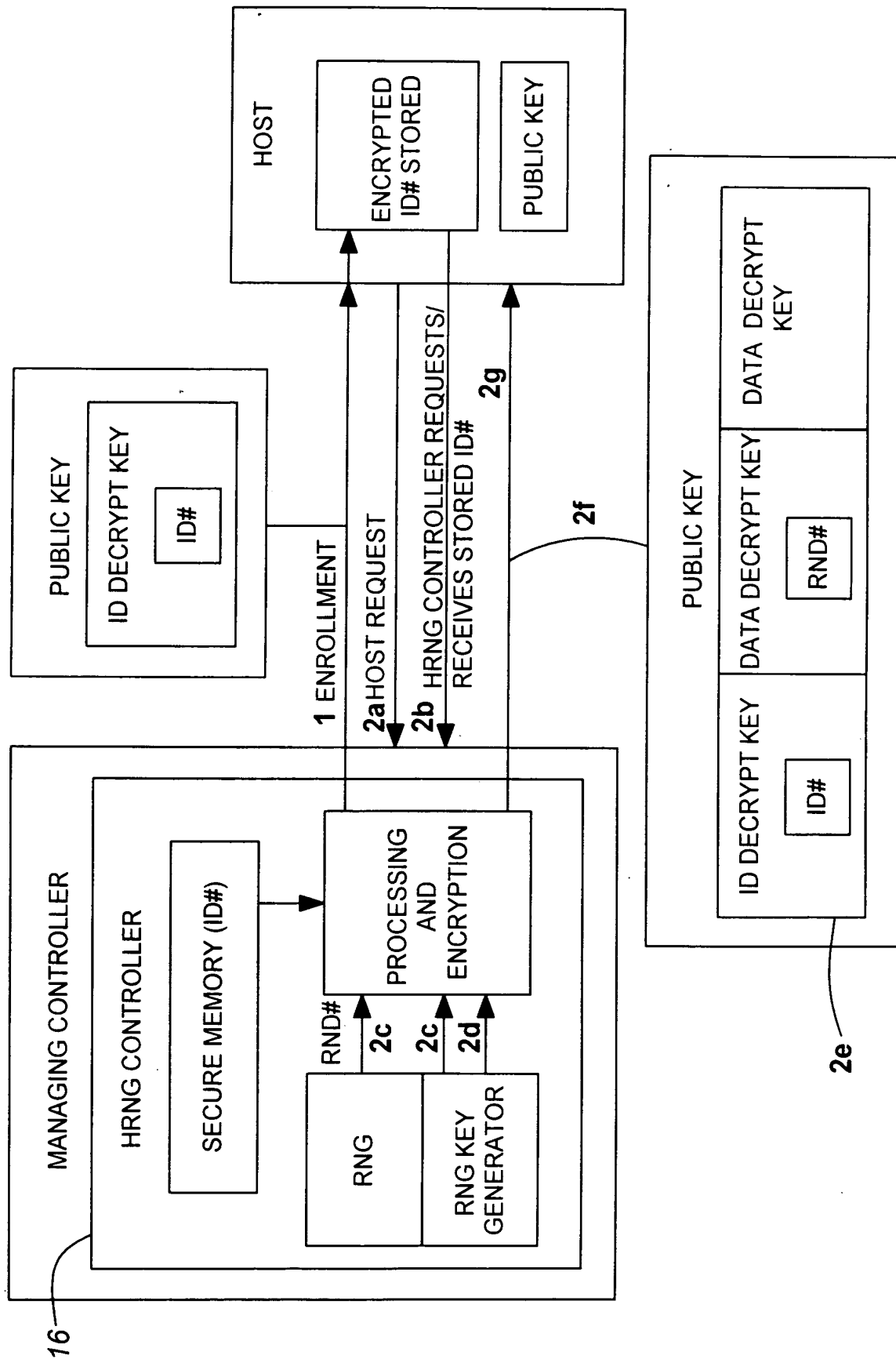
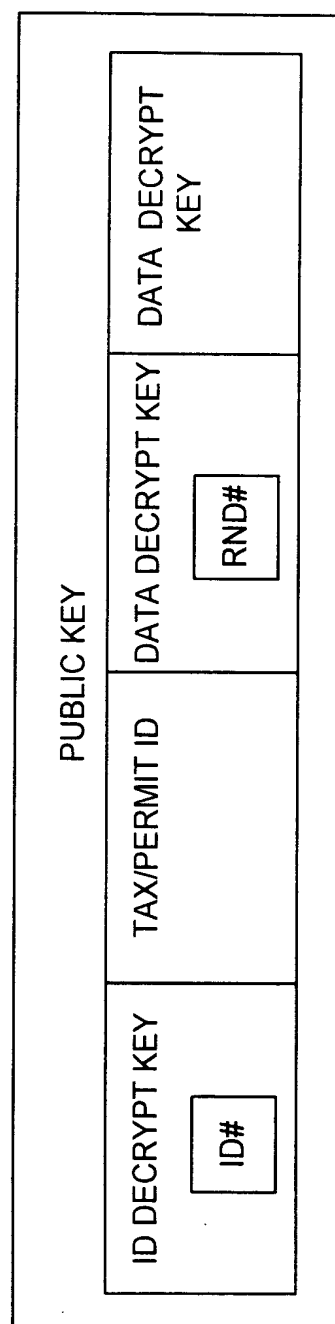
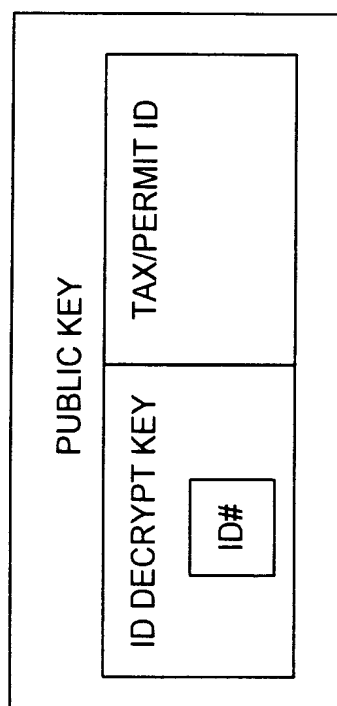
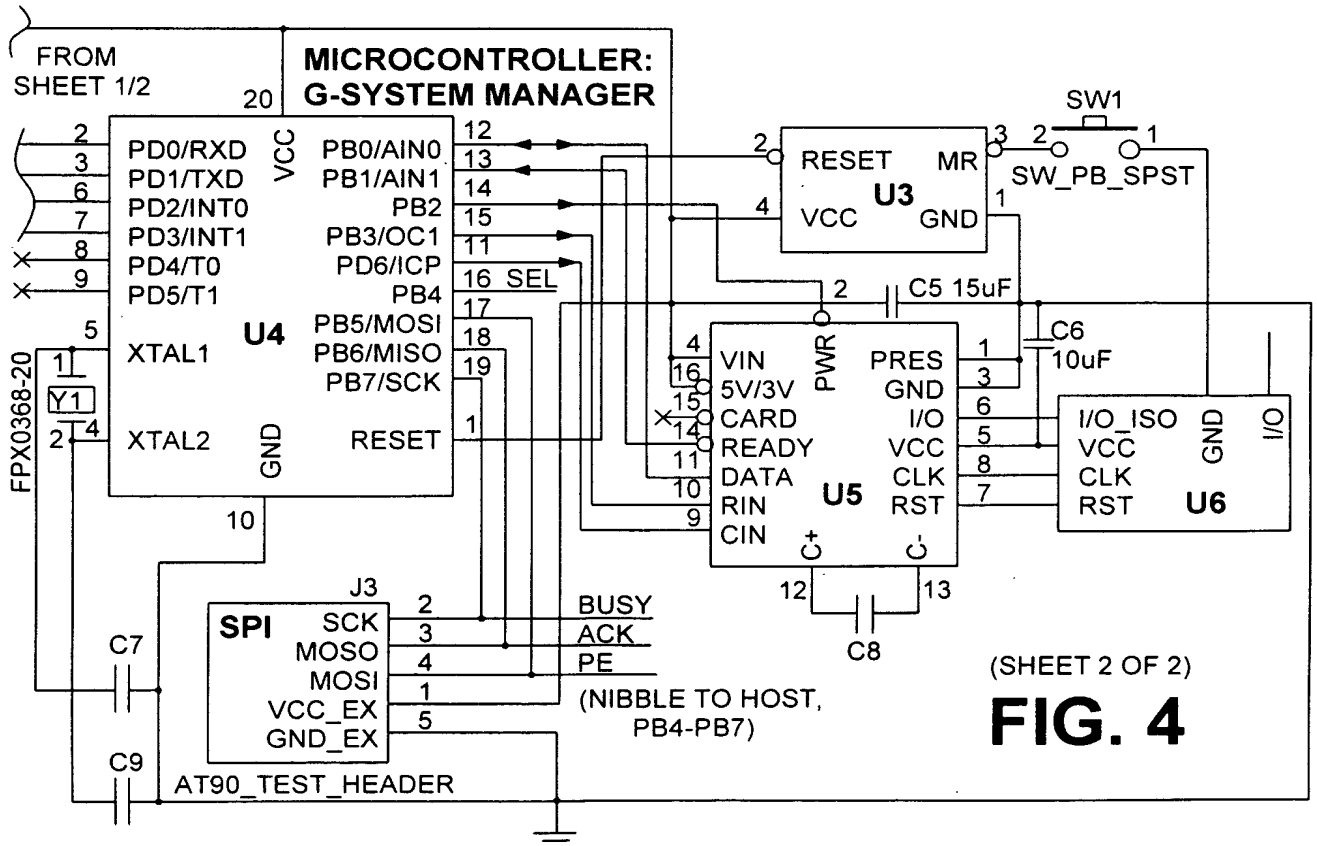
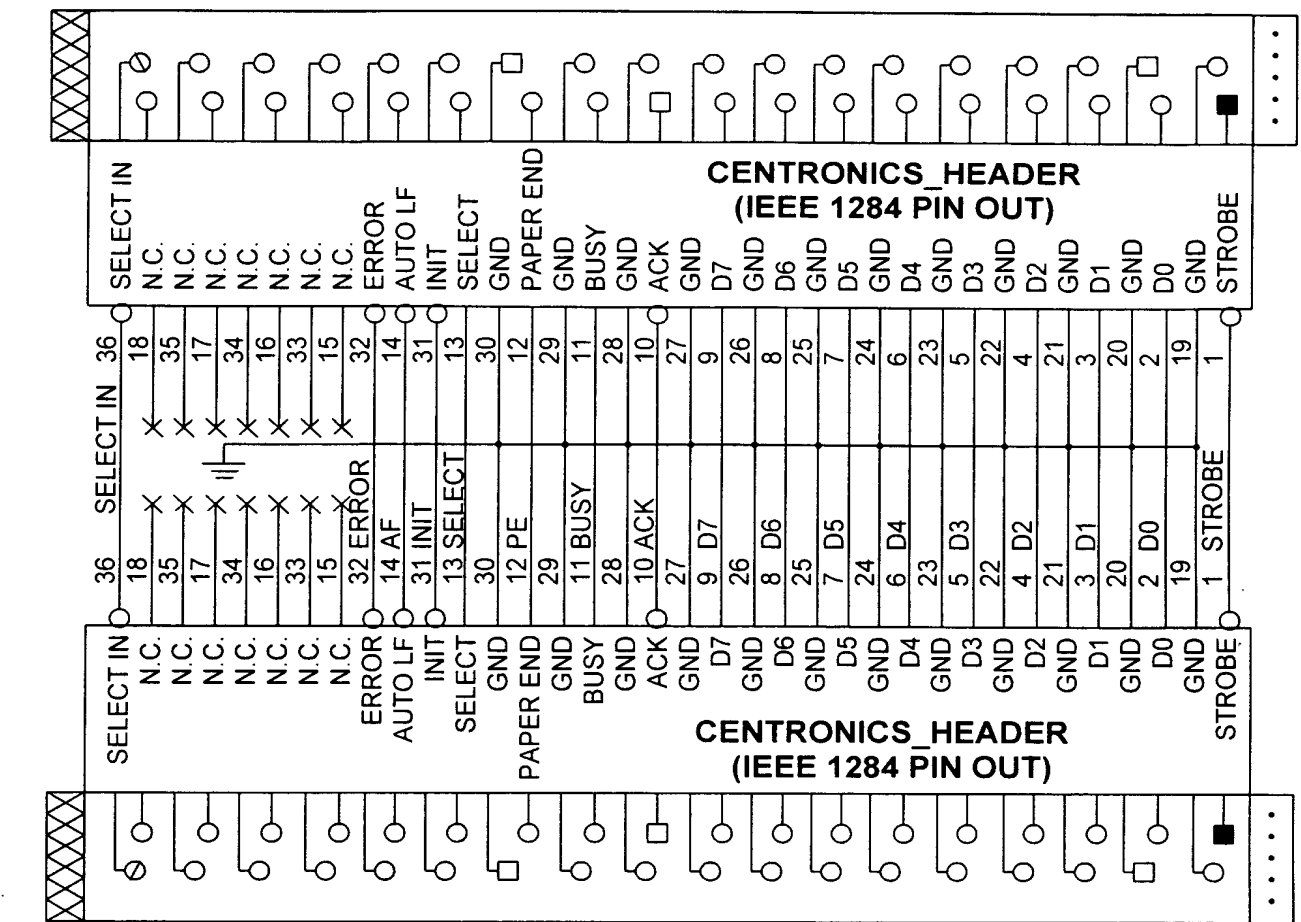


FIG. 2



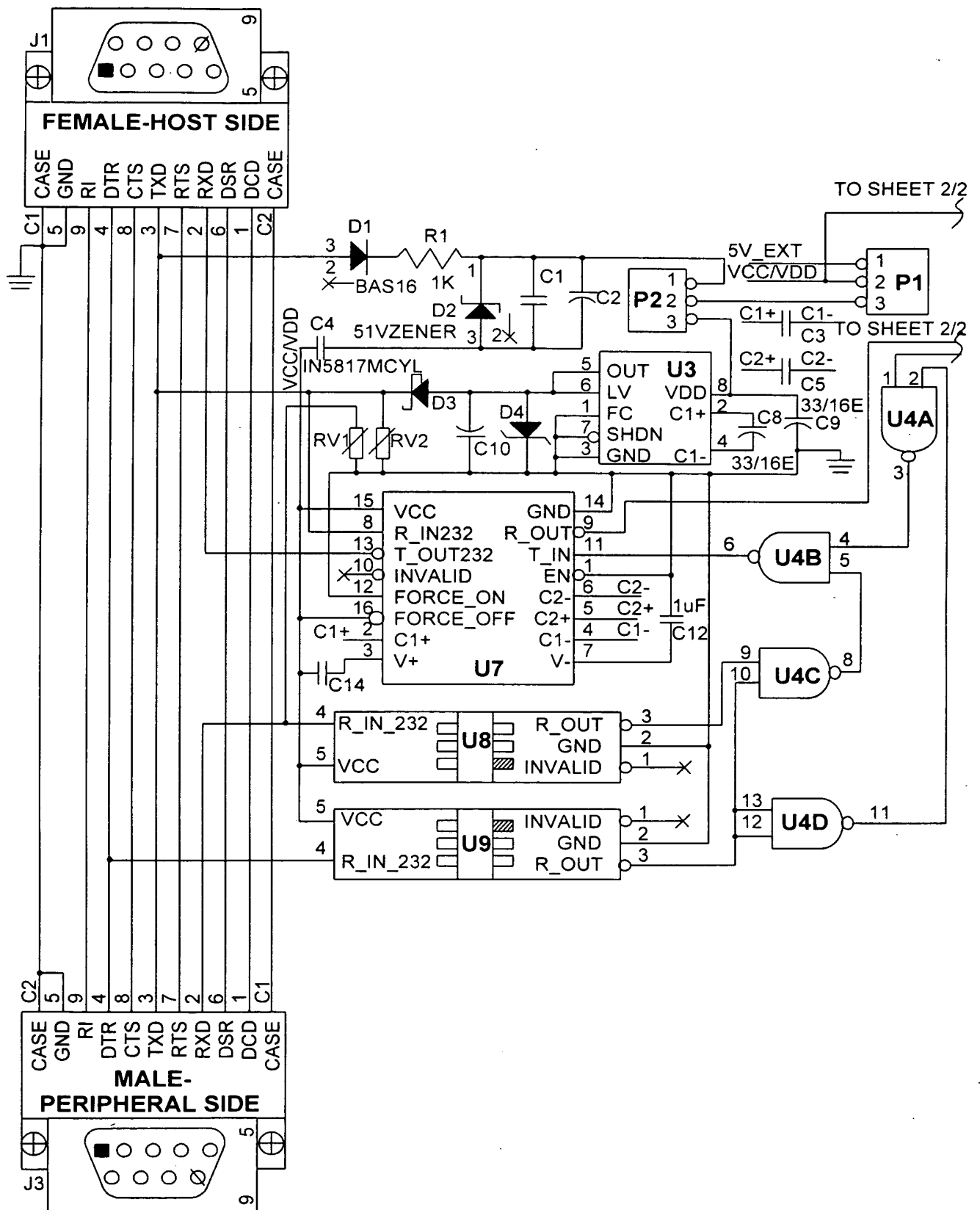


1. This circuit is a test circuit for the AT90C128-20 microcontroller. It is designed to test the microcontroller's ability to interface with a Centronics printer. The circuit includes a microcontroller, a Centronics header, a reset switch, a power switch, and a test header.



(SHEET 2 OF 2)

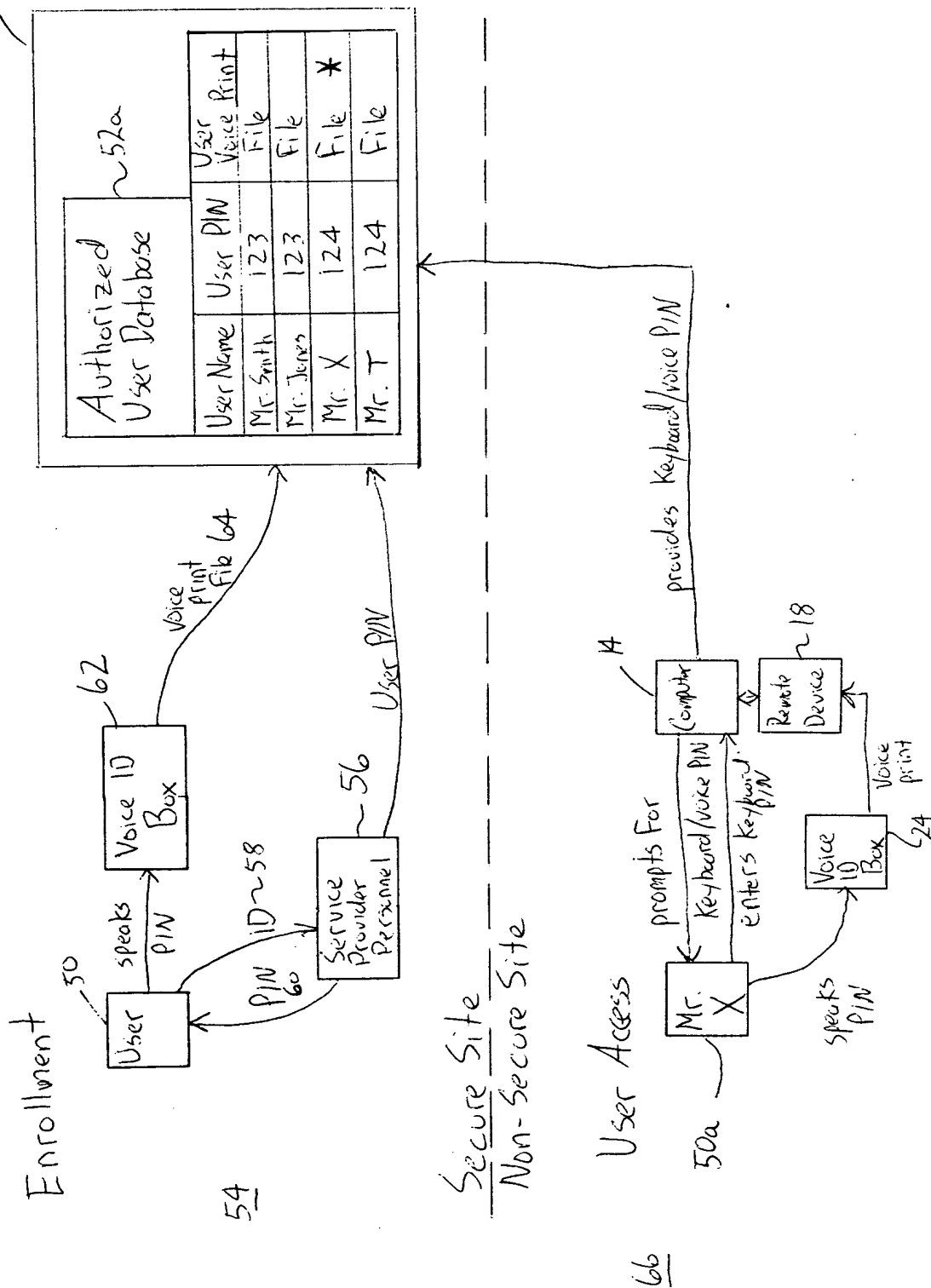
FIG. 4



(SHEET 1 OF 2)

FIG. 5

Figure 6



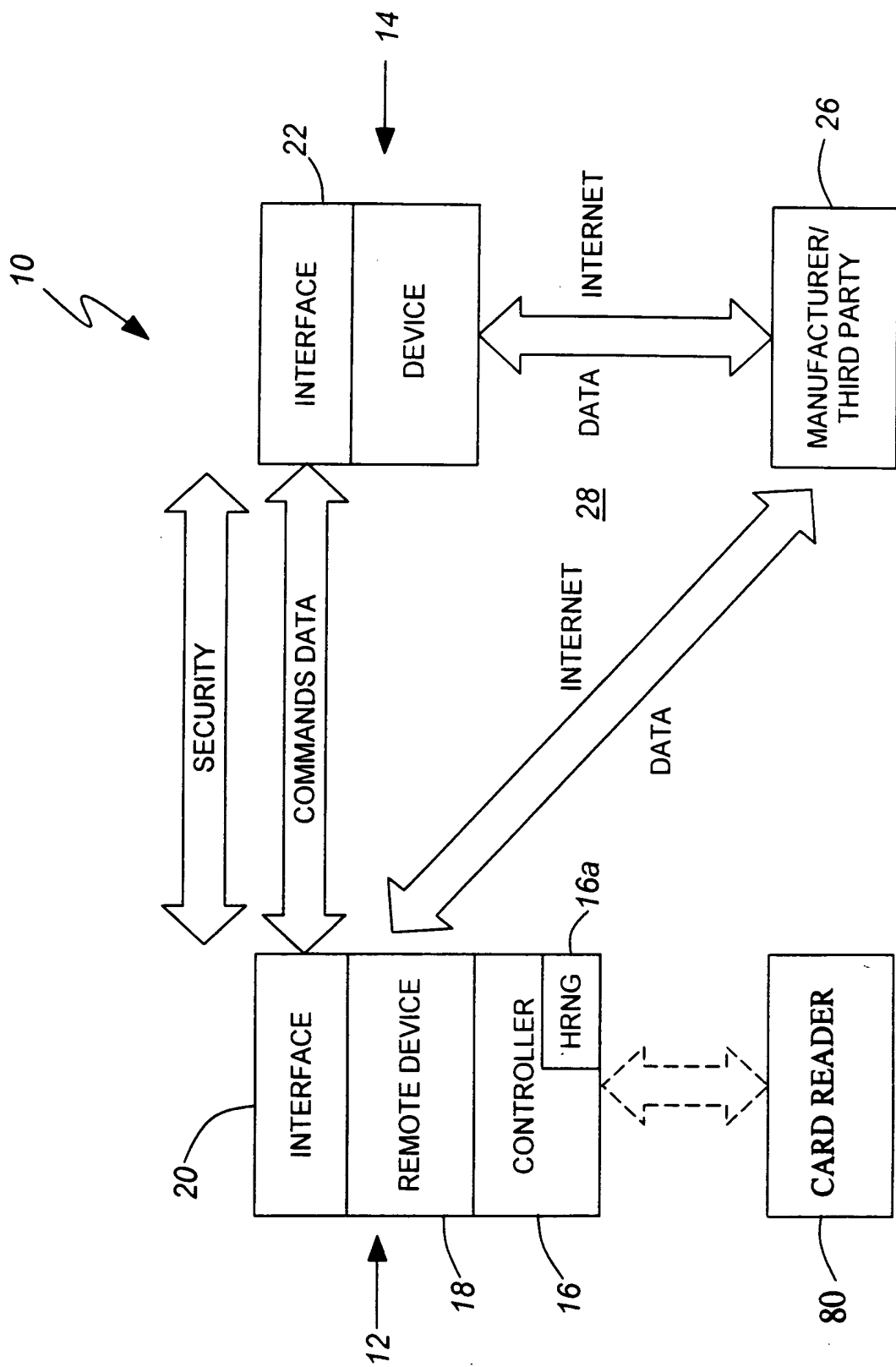


FIG. 7

Figure 8

Device Enrollment & Transaction Authentication

